

### TOPICS TO BE COVERED

- DSP PROCESSORS.
- IMAGE PROCESSING.
- RTOS.
- DSP IN EMBEDDED SSTEMS.
- EMBEDDED 'C'.
- ARM & PIC MICROCONTROLLERS.
- REAL TIME SIGNAL PROCESSING.
- ADAPTIVE FILTERING.
- VLSI SIGNAL PROCESSING.

### HOW TO APPLY

Filled-in application in the attached prescribed format sponsored by the concerned authority should reach the Coordinator on or before 28/03/2011. Candidates may send advance copies to "[bimandebbarma@indiatimes.com](mailto:bimandebbarma@indiatimes.com)". Selected candidates will be intimated by e-mail.

### OBJECTIVES OF THE SDP

- EXPOSURE TO RECENT DEVELOPMENTS IN DSP AND EMBEDDED SYSTEMS.
- MOTIVATION TO EXPLORE NEW FIELDS IN DSP & EMBEDDED SYSTEMS.

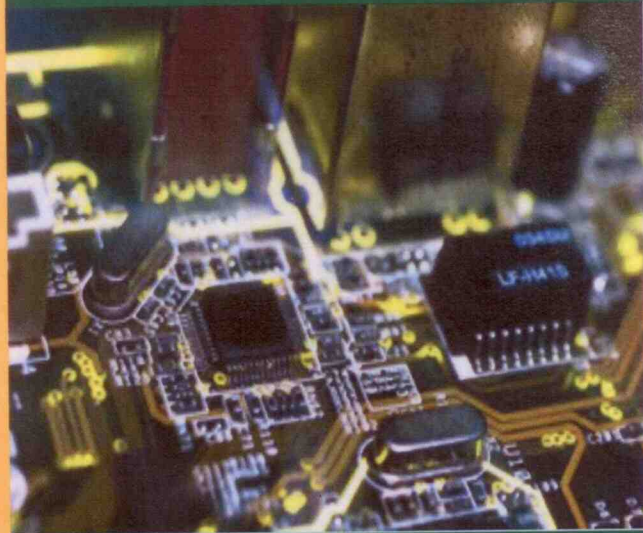
### IMPORTANT DATES

- Last date of submission of application—25/03/2011
- Intimation of acceptance—28/03/2011.

AICTE-MHRD Sponsored  
Staff Development Program

ON  
DSP & EMBEDDED SYSTEMS

5th April To 16th April, 2011



Coordinator:

Shri Biman Debbarma  
Asst. Professor. ECE Dept.

Conducted by:

ECE Department  
National Institute Of Technology, Agartala  
Agartala, Tripura(W)-799055  
Tel- +91-381-2346630  
Fax- +91-381-2346360  
Url- <http://www.nitagartala.in>

### ***ELIGIBILITY***

The Staff Development Program is open to faculty members from AICTE approved Engineering Colleges. Sponsored applicants working in AICTE approved engineering colleges are only eligible for TA, free boarding & lodging. Selection will be on first come first served basis and subject to AICTE norms.

### ***ACCOMODATION***

Accommodation for selected candidates will be provided in the NIT hostels and guesthouse.

### ***TA FOR PARTICIPANTS***

TA will be reimbursed as per AICTE norms.

### ***RESOURCE PERSONS***

Sessions will be handled by external experts from reputed institutions, industries and academicians from engineering colleges besides internal resource persons.

### ***ABOUT THE INSTITUTE***

The National Institute Of Technology Agartala is one of the 20 National Level Institute setup by the Government of India. The Institute 's mission is "To help to improve the economic development of the country particularly the North-Eastern states and also the public systems, through pursuit of excellence in technical education, research, consultancy and training". National Institute Of Technology Agartala, a centrally funded institution, is setup to impart quality technical education into various levels of higher learning. The institute has been converted from State Engineering College to National Institute of Technology on 01.04.2006. The institute was established in 1965 with the three branches of Civil, Electrical and Mechanical Engineering. Presently the institute offers eight UG courses and various PG courses in several fields along with Doctoral and Post Doctoral Fellowships.

### ***HOW TO REACH US***

The Institute is 4 km off Assam-Agartala Highway ( NH-44 ) and about 20 km away from Agartala. Agartala is well connected by Railways, Road and air. Pre-paid taxi and auto service is available from Agartala Airport to the Institute. Jirania railway station is 2 km away from the Institute.

### ***WEATHER***

The weather in the beginning of April is expected to be pleasant and warm.

### ***ABOUT THE DEPARTMENT***

Electronics and Communication Engineering Department was established in 2005 with its name then Electrical & Electronics Eng. Department. It started its journey with two faculty members one of whom is the Coordinator of this program. Now this department has a total of twelve faculty members from different fields of Electronics & Communication Engineering. The department has also started a PG course on "Microelectronics & VLSI Technology" in the year 2010. The department of Electronics & Communication Engineering nurtures new ideas and inventions which ensures the qualities of future engineers in their growth from classroom to industries.

### ***ABOUT THE PROGRAM***

This Induction Training Program will upgrade the knowledge of DSP & EMBEDDED SYSTEMS of the participants as well as introduce the participant to the recent concepts and developments in its vast application areas. DSP is a multidisciplinary technology area which is directly or indirectly connected to modern day processing systems and Embedded Systems. This program will enhance the knowledge of Participants in all aspects. Further, this induction Training Program will help the participants for up-gradation of educational technology, motivation, communication skills and management.

AICTE-MHRD Sponsored  
Staff Development Program  
ON  
DSP & EMBEDDED SYSTEMS  
5th April To 16th April, 2011  
Application cum Registration form  
( USE BLOCK LETTERS ONLY)

NAME: \_\_\_\_\_

DATE OF BIRTH: \_\_\_\_\_

GENDER: MALE / FEMALE

DESIGNATION: \_\_\_\_\_

DEPARTMENT \_\_\_\_\_

HIGHEST QUALIFICATION: \_\_\_\_\_

EXPERIENCE( IN YEARS) :

TEACHING: : \_\_\_\_\_ INDUSTRY: : \_\_\_\_\_

INSTITUTION: \_\_\_\_\_

IS THE INSTITUTION AICTE APPROVED? Y/ N

MAILING ADDRESS: : \_\_\_\_\_

\_\_\_\_\_

PHONE:( OFFICE) \_\_\_\_\_

( RES/MOBILE) \_\_\_\_\_

E-MAIL: \_\_\_\_\_

WHETHER HOSTEL ACCOMODATION REQUIRED: Y / N

NOTE: brochure and application form can also be downloaded from the  
link—[www.nitagartala.in](http://www.nitagartala.in)

## ***DECLARATION BY THE CANDIDATE***

The information furnished is true to the best of my knowledge. I agree to abide by the rules and regulations governing the program. If selected, I shall attend the program for the entire duration.

Place:

Date:

Signature

## ***SPONSORSHIP CERTIFICATE***

Certify that Mr./Mrs./Dr \_\_\_\_\_

is an employee of our institution and hereby sponsored for the Staff Development Program on " DSP & Embedded Systems" from 05.04.2011 to 16.04.2011. He / She will be permitted to attend the program fully, if selected.

Place:

Date:

Signature of the  
Sponsoring authority & seal

Address fro Correspondance:  
**Shri Biman Debbarma**

Asst. Professor, ECE Dept.

National Institute Of Technology, Agartala

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