

TOPICS TO BE COVERED

- Modern Power Electronics Devices
- 1 \emptyset , 3 \emptyset AC to DC Converters
- DC to DC converters, Choppers, Buck, Boost, Buck Boost, Cuk converters
- Resonant converters
- Inverters, Multi level Inverters
- Advanced PWM techniques for Inverters
- DC Drives: — with converters, Choppers, Closed loop operation, simulation.
- AC Drives: — IM drives, SM drives, v/f control, vector control, DTC, simulation.
- Controller design and modern control strategy
- Protection issues of Power Electronics converters

How to Apply:

Filled-in application in the attached prescribed format sponsored by the concerned authority should reach to the co-ordinator on or before 01.02.2011. Candidates may send the advance copy through E-mail. Selected candidates will be intimated only through E-mail by 07.02.2011.

OBJECTIVES OF THE SDP

- To create an awareness of Recent Trends in Power Electronics and Drives among the faculty members.
- To motivate them in different field of Power Electronics application
- To transform data into information and information into knowledge.

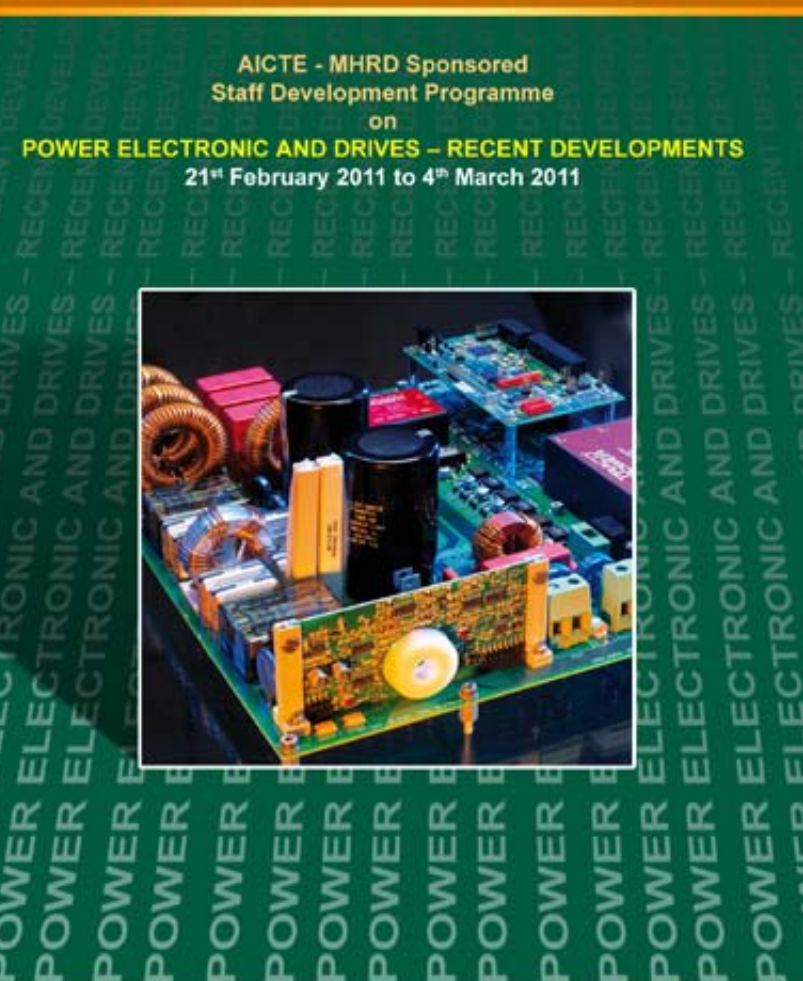
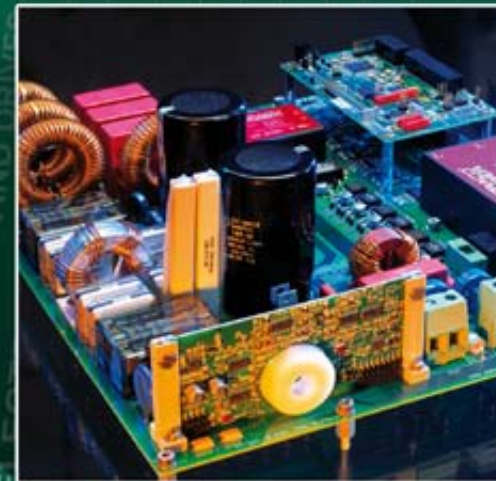
DATES TO REMEMBER

Last date for submission of Application: 01.02.2011
Intimation of acceptance (by email only) : 07.02.2011

AICTE - MHRD Sponsored
Staff Development Programme
on

POWER ELECTRONIC AND DRIVES – RECENT DEVELOPMENTS

21st February 2011 to 4th March 2011



Coordinator
Smt. Anindita Jamatia
Assistant Professor, EE Dept.



Conducted by
DEPARTMENT OF ELECTRICAL ENGINEERING
National Institute of Technology, Agartala
Agartala, Tripura(W)-799055
Tel: +91-0381-2346630, Fax: +91-0381-2346360
url: <http://www.nitagartala.in>

AICTE Sponsored Staff Development Programme On
Power Electronics and Drives - Recent Developments
21st February,2011 – 4th March, 2011

APPLICATION Cum REGISTRATION FORM
(Use BLOCK Letters only)

Name :

Date of Birth:

Gender: MALE/ FEMALE

Designation:.....

Department:.....

Highest Qualification:

Experience (in years) Teaching:.....Industry.....

Institution:.....

Is the Institution AICTE approved? Yes/No

Mailing address.....

Tel:(Office)..... Res/Mobile.....

E-mail:.....

Whether Hostel accommodation required- Yes / No

Note:-Xerox copy of the application form can also be used for applying. Brochure & application form can also be downloaded from the following link: www.nitagartala.in

Declaration by the Candidate

The information furnished is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the programme for the entire duration.

Place:

Date :

Signature

Sponsorship Certificate

Certify that Mr./Ms/Dr.....
..... is an employee of our institution and hereby sponsored for the Staff Development Programme on Power Electronics and Drives - Recent Developments 21st February,2011 – 4th March, 2011. He/She will be permitted to attend the programme fully, if selected.

Place:-

Date:-

Signature of the
sponsoring authority & Seal

Address for Correspondence:

Smt. Anindita Jamatia,
Co-ordinator,

SDP on "**Power Electronics and Drives – Recent Developments.**"

Electrical Engineering Department,
NIT, Agartala

Jirania-799055, Tripura

E-mail: anindita.jamatia@gmail.com

Phone : 0381-2346629/0381-2346630

Mobile :09862115012

Fax : 0381-2346360

ELIGIBILITY

The staff development programme is open to faculty members from AICTE approved Engineering Colleges. sponsored applicants working in AICTE approved engineering colleges are only eligible for the TA, free Boarding & Lodging. Selection will be on first come first served basis and subject to AICTE norms.

ACCOMMODATION

Accommodation for selected candidates will be provided in the NIT hostels and guesthouse.

TA FOR PARTICIPANTS

Maximum by 3-tier AC through shortest route will be paid to the participants on production of original tickets. TA will be reimbursed as per AICTE norms.

RESOURCE PERSONS

Sessions will be handled by external experts from Reputed Institutions, Industries, Universities and Academicians from engineering colleges besides internal resource persons.

ABOUT THE INSTITUTE

The National Institute of Technology Agartala is one of the 20 (twenty) National Level Technical Institutes setup by the Govt. of India. The Institute's mission is "To help to improve the economic development of the country particularly the North-East states and also the public systems, through pursuit of excellence in technical education, research, consultancy and training".

National Institute of Technology Agartala, a centrally funded Institution is setup to impart quality technical education into various levels of higher learning. The Institute has been converted from State Engineering College to National Institute of Technology on 01.04.2006. The Institute (i.e. erstwhile Tripura Engineering College) was established in 1965 with the three traditional branches of Civil, Electrical and Mechanical Engineering. Presently the Institute offers eight UG courses and PG courses in several fields along with Post Doctoral Fellowships.

HOW TO REACH US

The Institute is 4 km off Assam-Agartala Highway (NH-44) and about 20 km away from Agartala. Agartala is well connected by Rail, Road and Air. Pre-paid Taxi and auto service is available from Agartala Airport to Institute. Jirania rail station is 2 KM away from Institute.

WEATHER

The weather in the end of February is expected to be pleasant. Light winter cloths may require.

ABOUT THE DEPARTMENT

Electrical Engineering Department is among the first three departments with which Tripura Engineering college, which has been converted to NIT, started its journey in the year 1965. Since its inception Electrical Engineering Department has always been known for its academic excellence. The mission of the department is to help the society by educating engineers with the exposure to latest technology and well equipped lab to administer and solve real world problem. Beside the UG course the department offers PG course in several fields. Electrical Engineering Department of NITA nurtures new ideas and inventions which ensures the qualities of future engineers in their growth from classroom to industry.

ABOUT THE PROGRAMME

In recent years the field of Power Electronics has experienced a large growth due to the revolutionary advancement of semiconductor fabrication technology as well as development of microelectronics methods. The Staff Development Programme on "POWER ELECTRONIC AND DRIVES – RECENT DEVELOPMENTS" is de-signed to update the knowledge of the participants in the field of high performance power electronics device, signal processors, their application in power system, process control and automation etc. Participants will be given adequate exposure to modern equipments of power electronics laboratory and different simulation softwares. The content of the course will also help the participants to target research problems.